

OCT 04 2005

Attorney's Docket No.: 10559-515001  
Client's Ref. No.: P12419  
Intel Corporation

**OFFICIAL COMMUNICATION FACSIMILE:**

**OFFICIAL FAX NO: (571) 273-8300**

Number of pages including this page **22**

Applicant : Eidson et al.  
Serial No. : 09/994,483  
Filed : November 26, 2001

Art Unit : 2183  
Examiner : Kevin P. Rizzuto  
Assignee : Intel Corporation

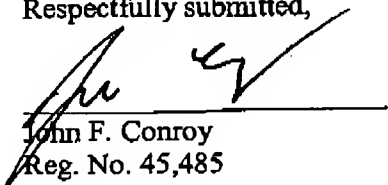
Title : System-On-Chip Breakpoint Synchronization

**Mail Stop AF**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Attached to this facsimile communication cover sheet is an Amendment, faxed this 4<sup>th</sup> day of October, 2005, to the United States Patent and Trademark Office.

Respectfully submitted,

Date: October 4, 2005

  
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002/022

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Serial No.: 09/994,483 Examiner: Kevin P. Rizzuto  
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Title : SYSTEM-ON-CHIP BREAKPOINT SYNCHRONIZATION

VIA FACSIMILE

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Commissioner For Patents  
P.O. Box 1450  
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AMENDMENT

In response to the Office action mailed August 4, 2005,  
please reconsider the application in light of the following:

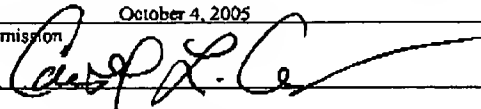
Amendments to the Specification beginning on page 2;

Amendments to the Claims reflected in the Listing of Claims  
beginning on page 3; and

Remarks beginning on page 15.

CERTIFICATE OF TRANSMISSION BY FACSIMILE

I hereby certify that this correspondence is being transmitted by  
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Date of Transmission October 4, 2005  
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